

FIG. 1

CONFIDENTIAL

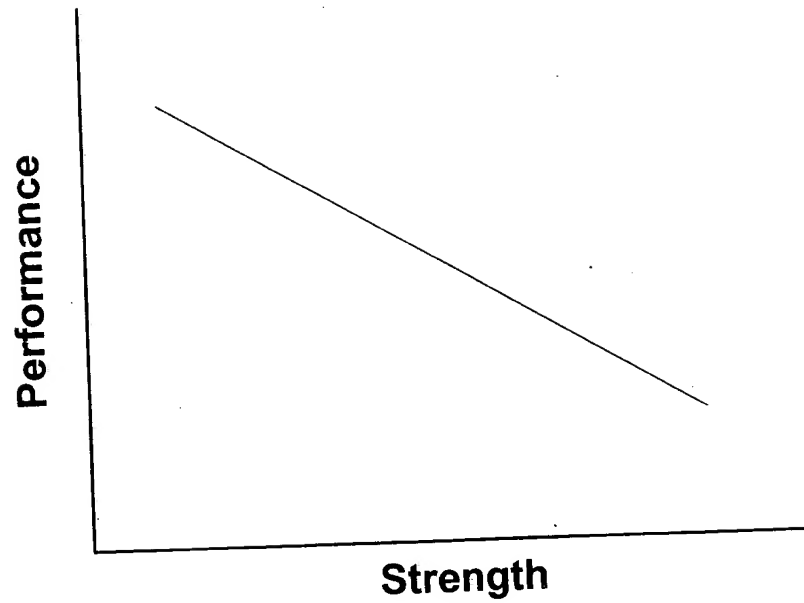


FIG. 2

300

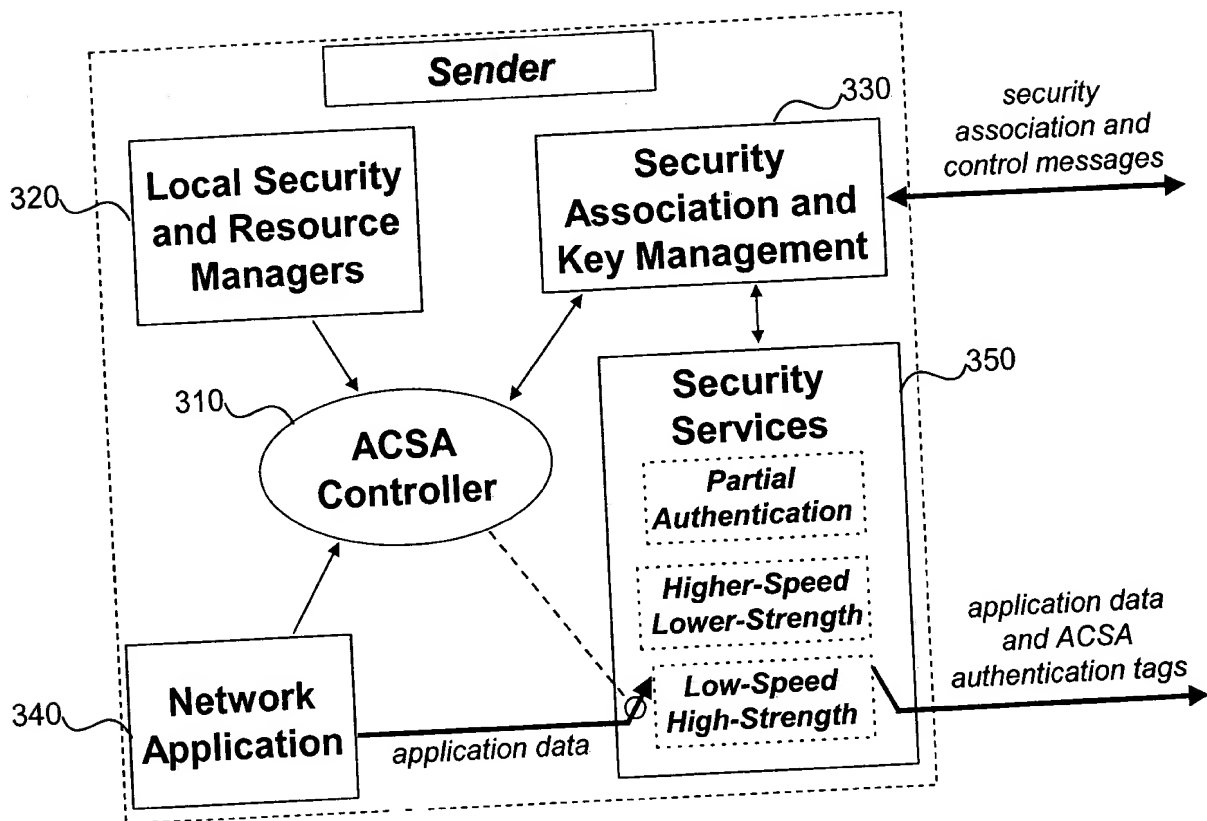


FIG. 3

402 404 406 408 410 412

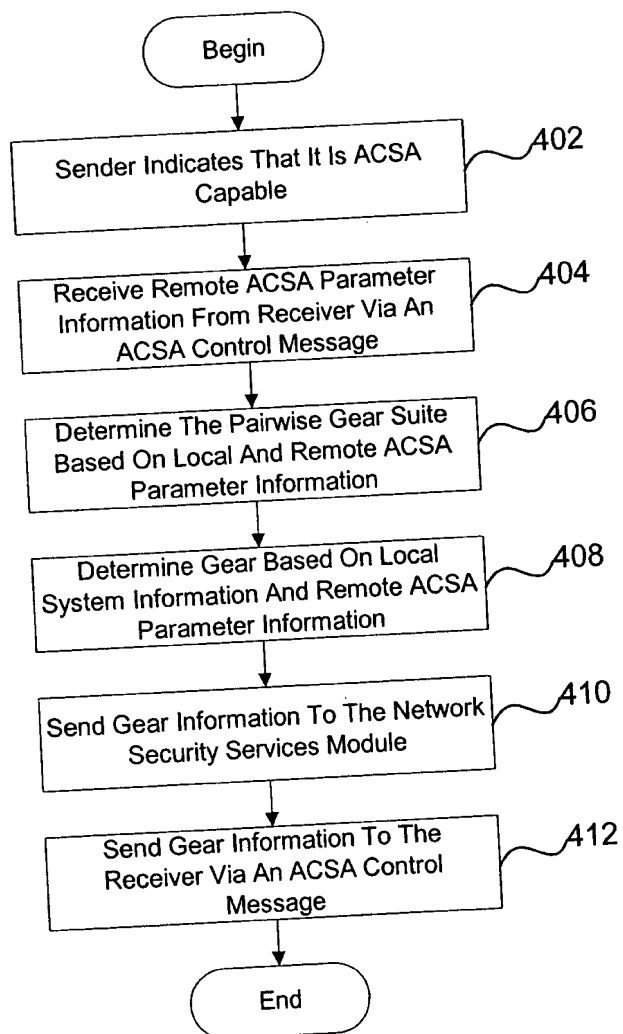


FIG. 4

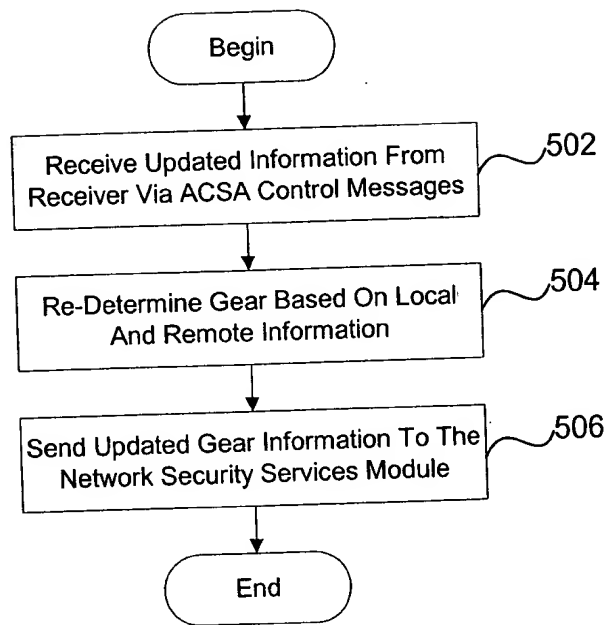


FIG. 5

SECRET

		Receiver Processor Utilization		
Sender Processor Utilization		Too Heavily Loaded	Near Desired CPU Load	Lightly Loaded
	Too Heavily Loaded	switch to less computationally intensive gear	switch to less computationally intensive gear	switch to less computationally intensive gear
	Near Desired CPU Load	switch to less computationally intensive gear	maintain current gear	maintain current gear
	Lightly Loaded	switch to less computationally intensive gear	maintain current gear	switch to more secure (more computationally intensive) gear

FIG. 6

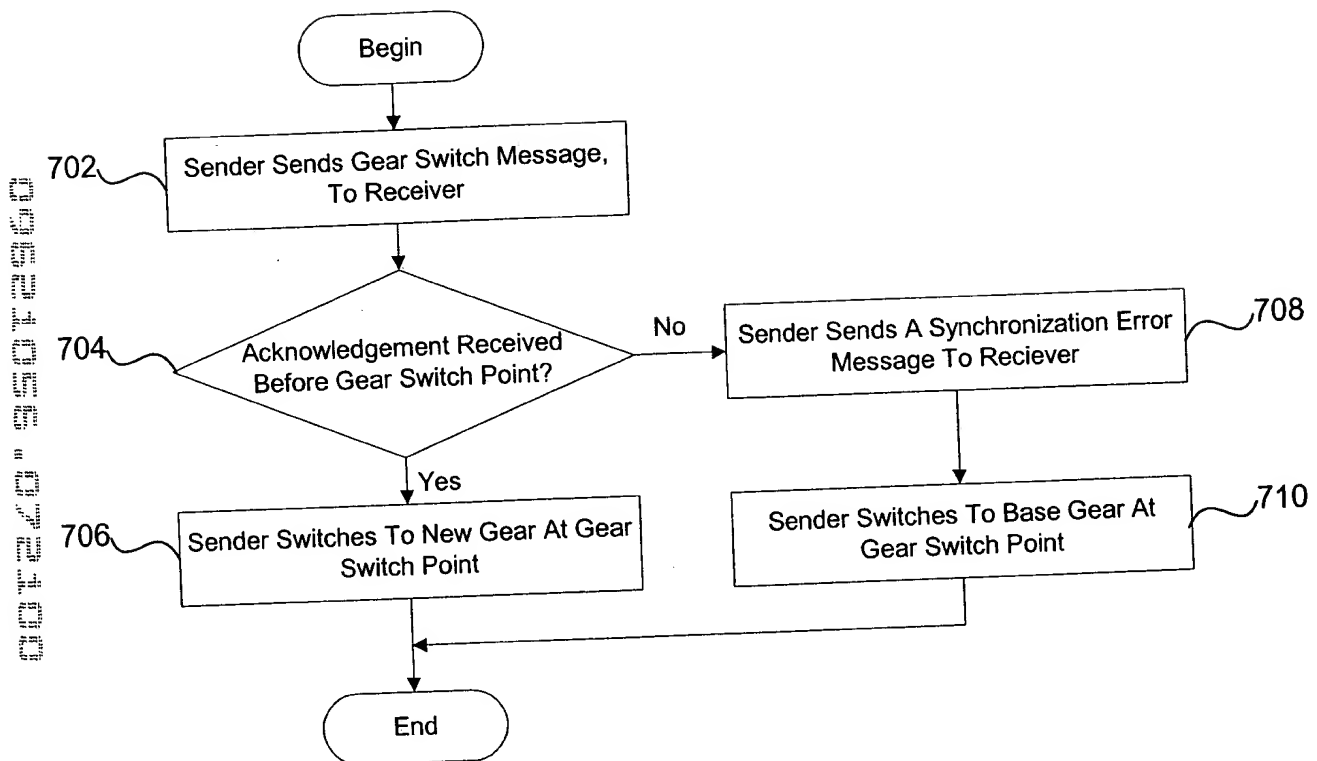


FIG. 7A

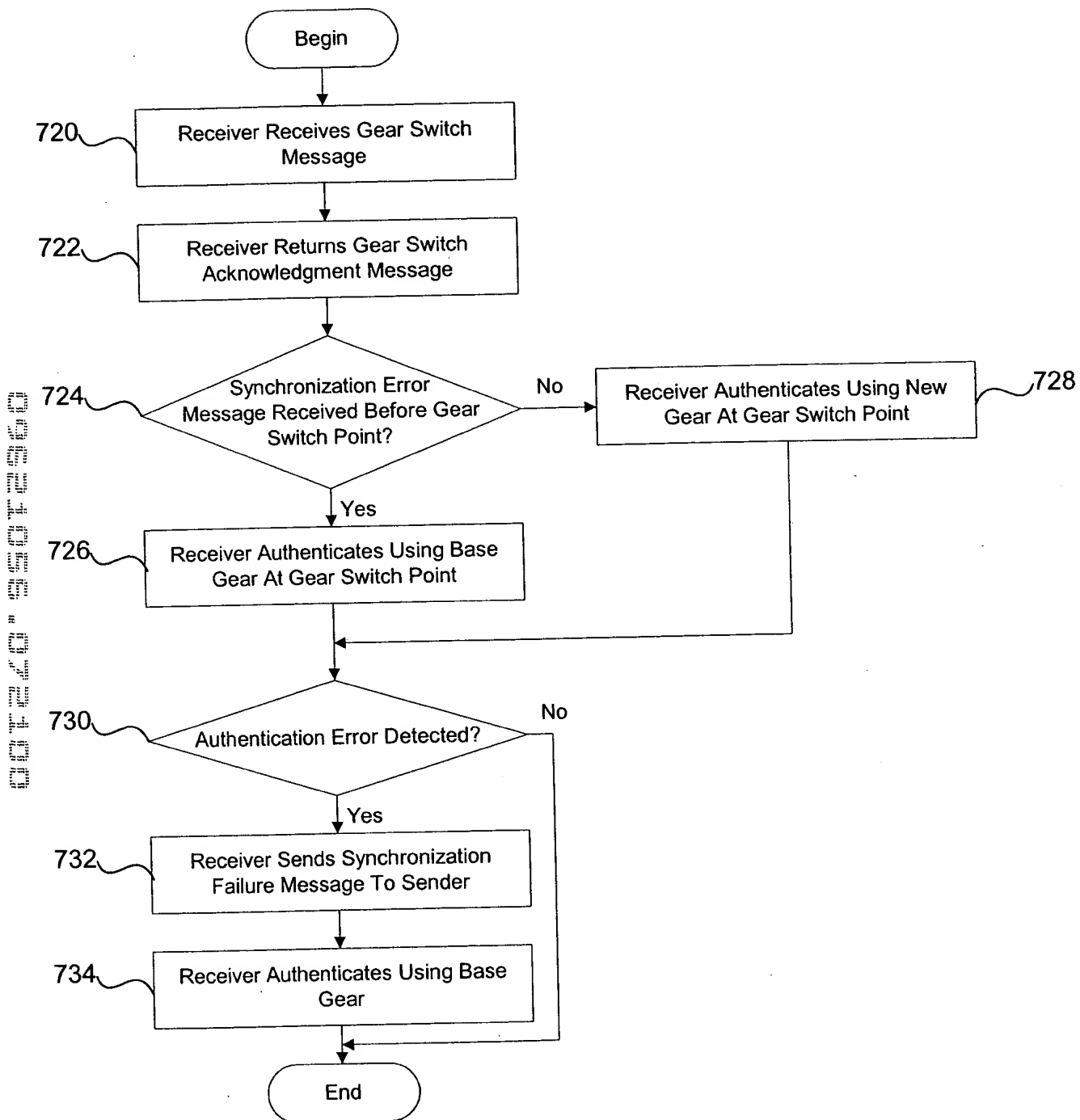


FIG. 7B

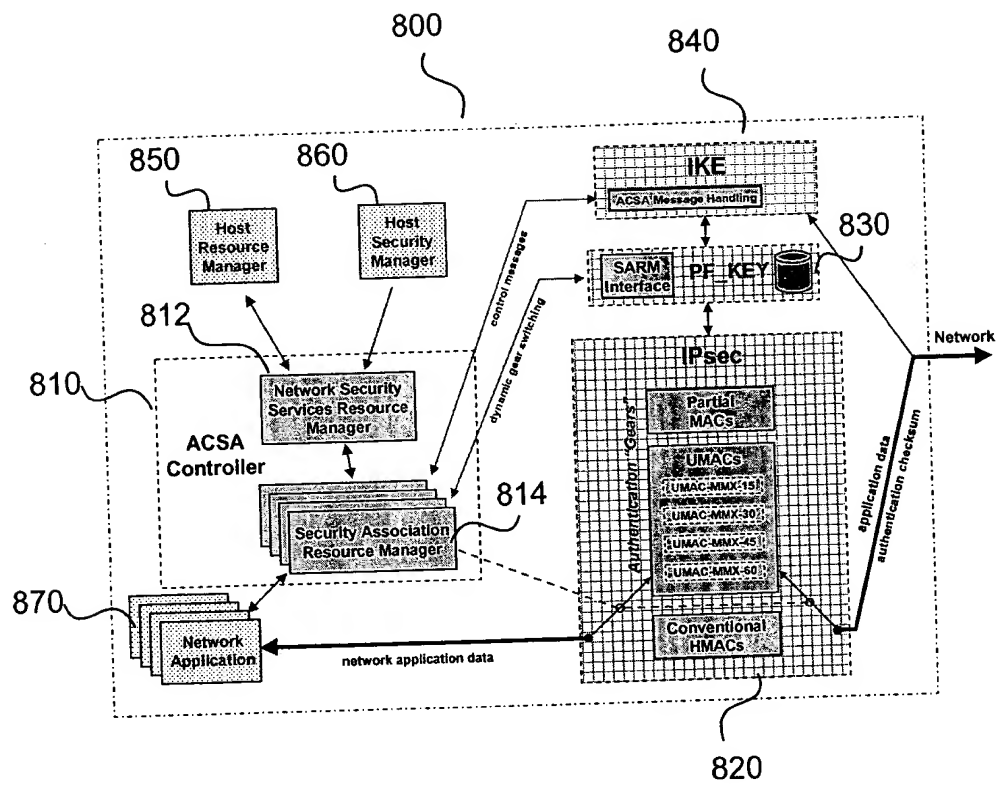


FIG. 8

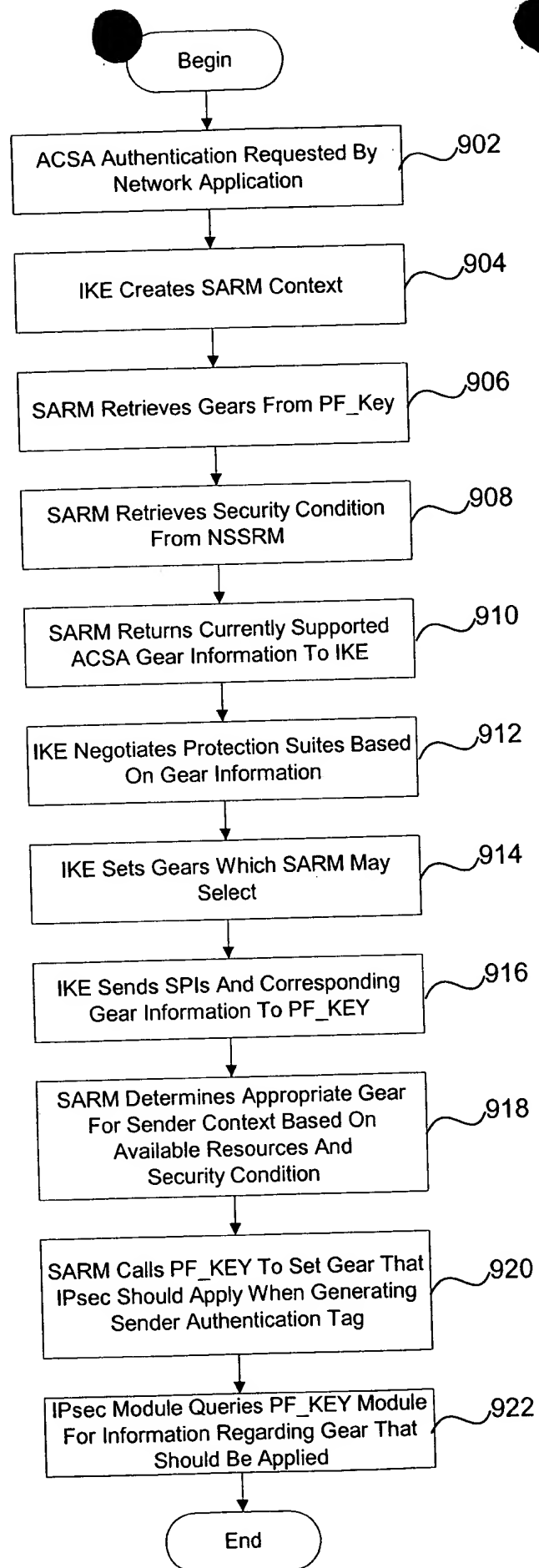


FIG. 9

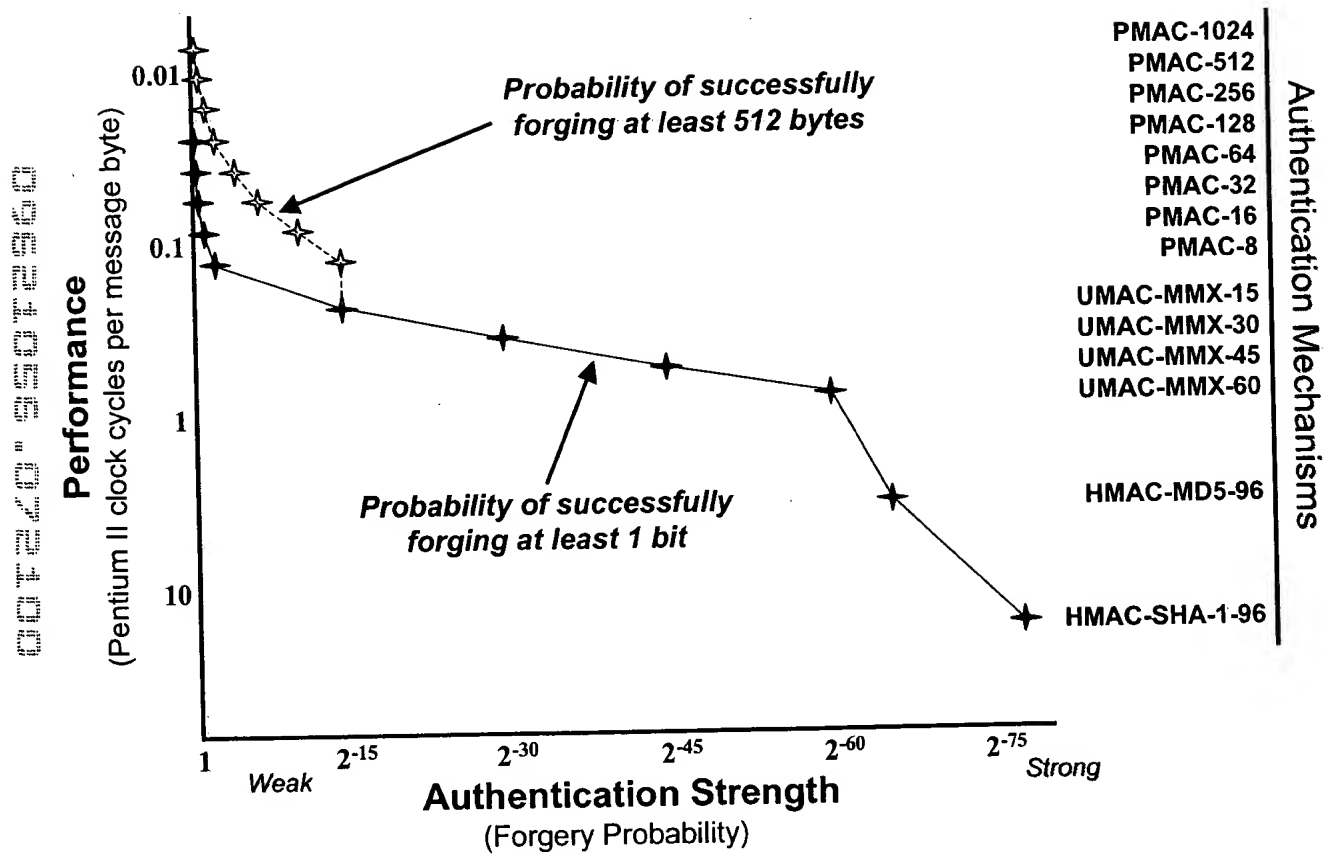


FIG. 10

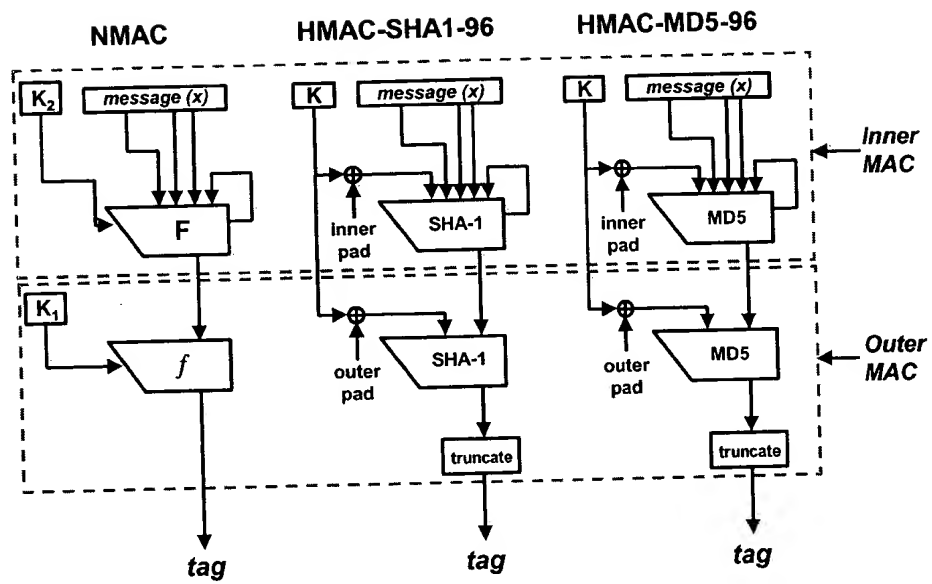


FIG. 11

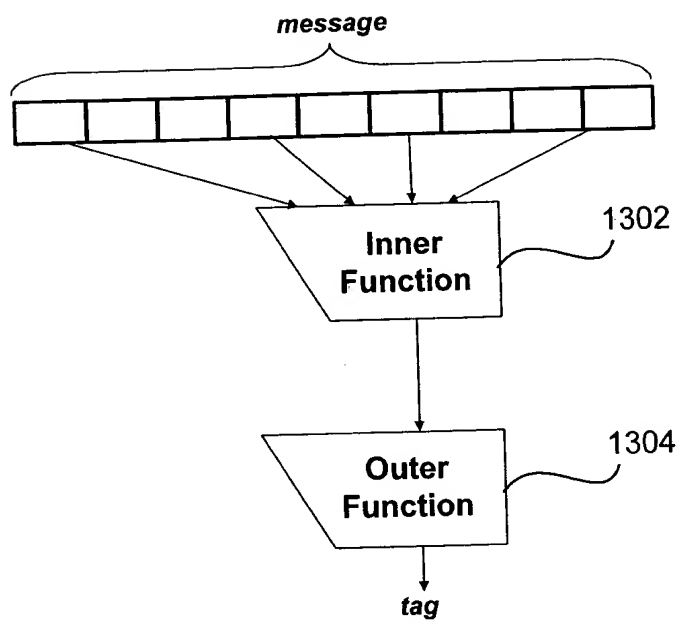


FIG. 13

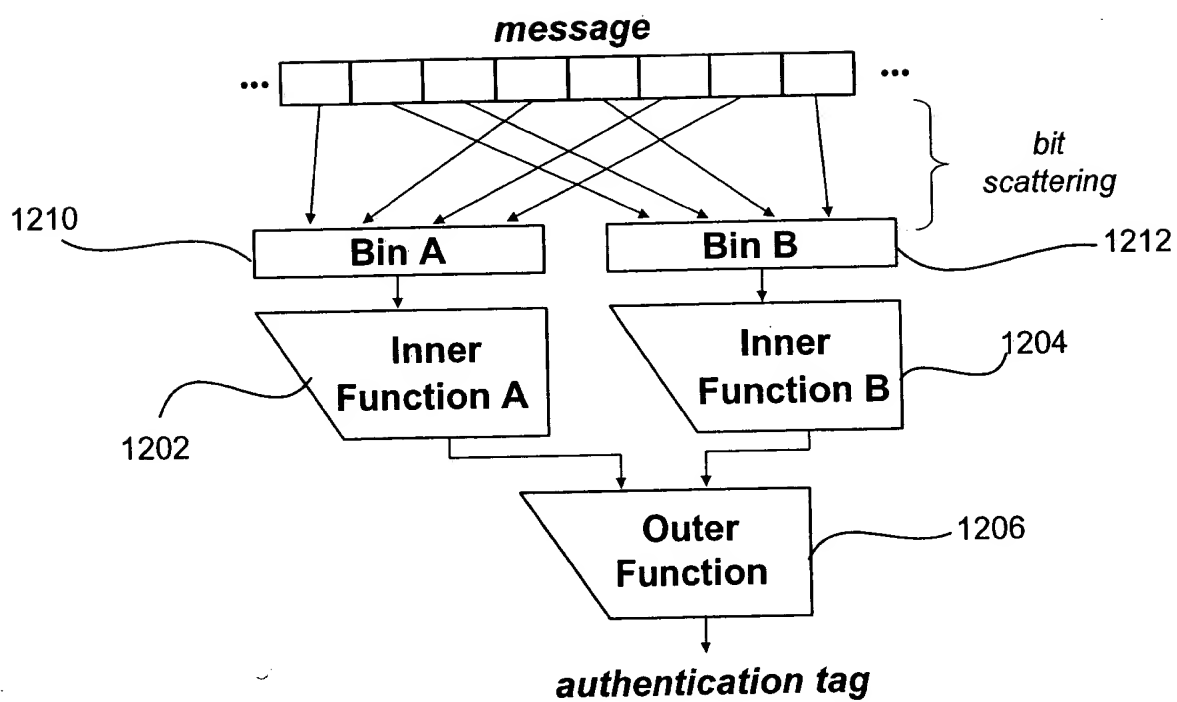


FIG. 12

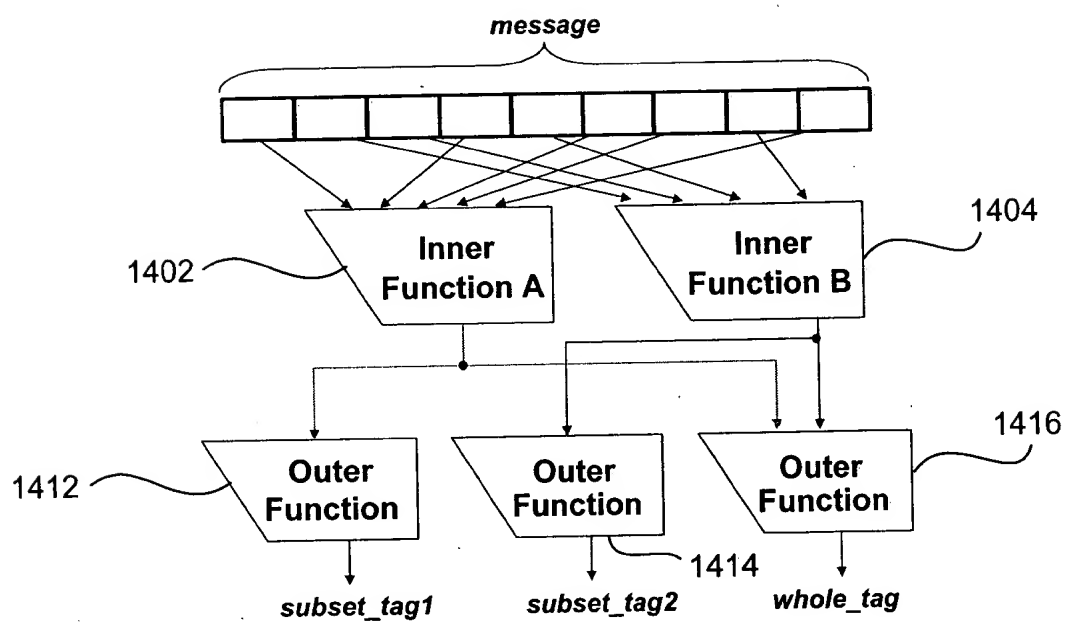


FIG. 14

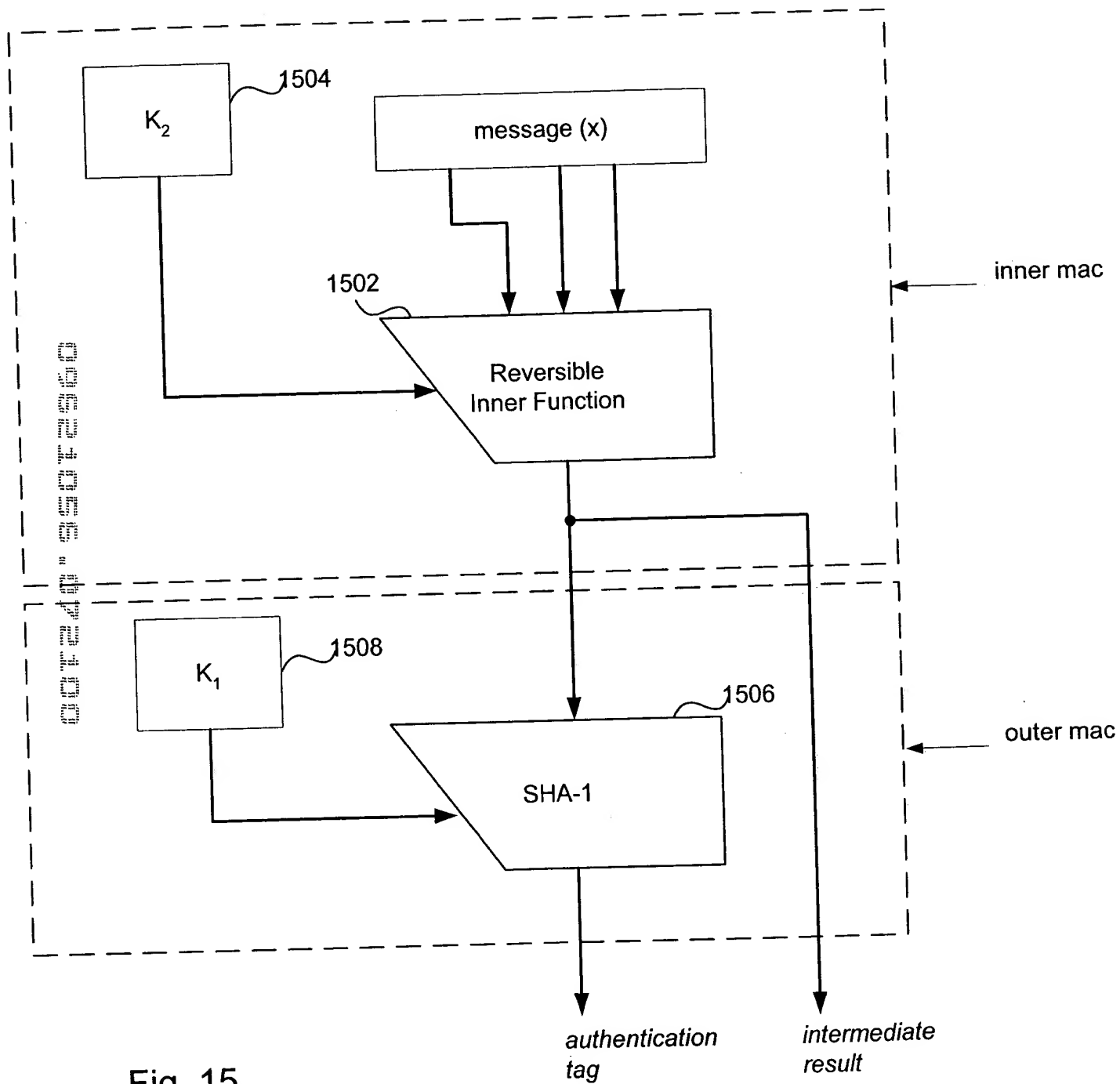


Fig. 15

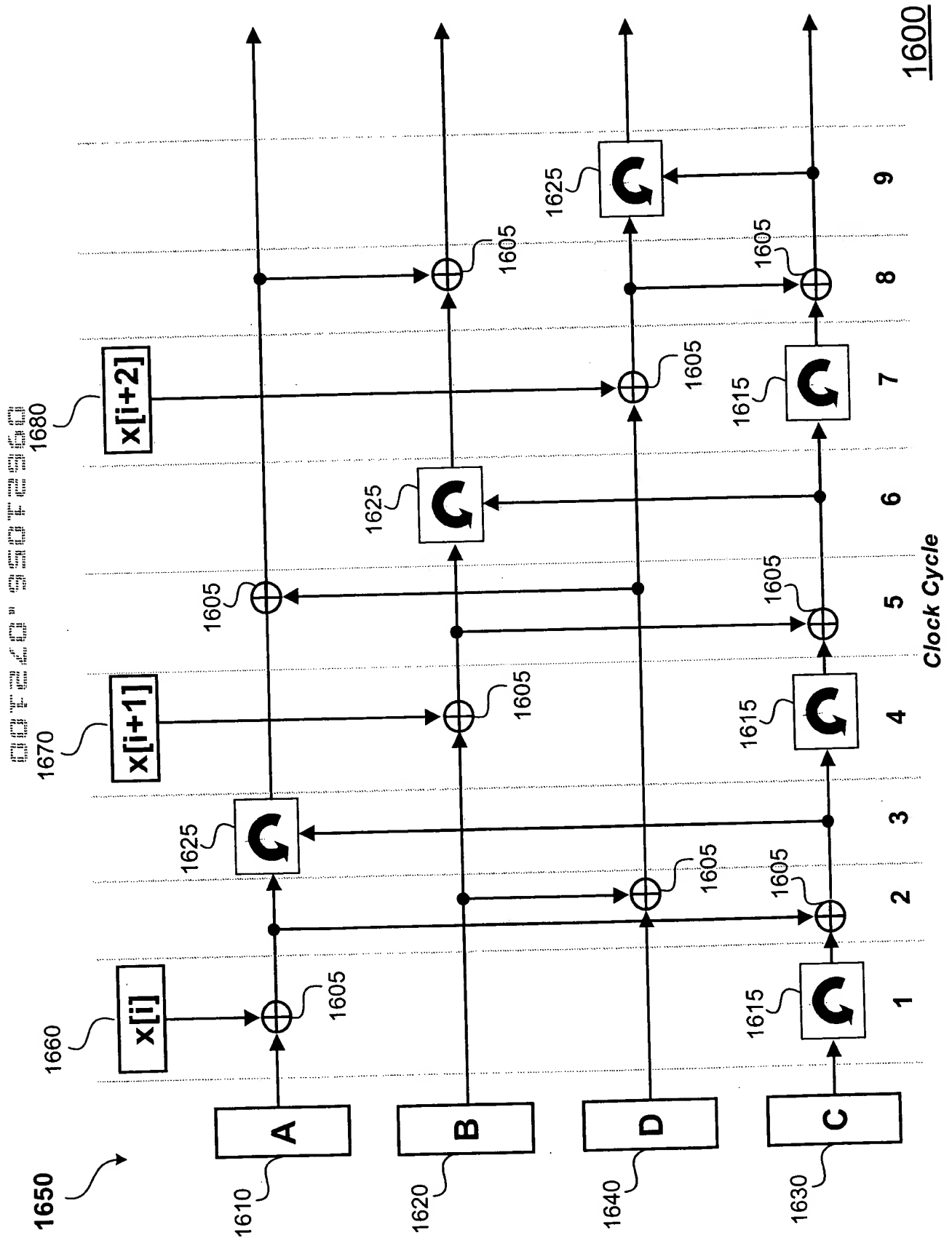


FIG. 16

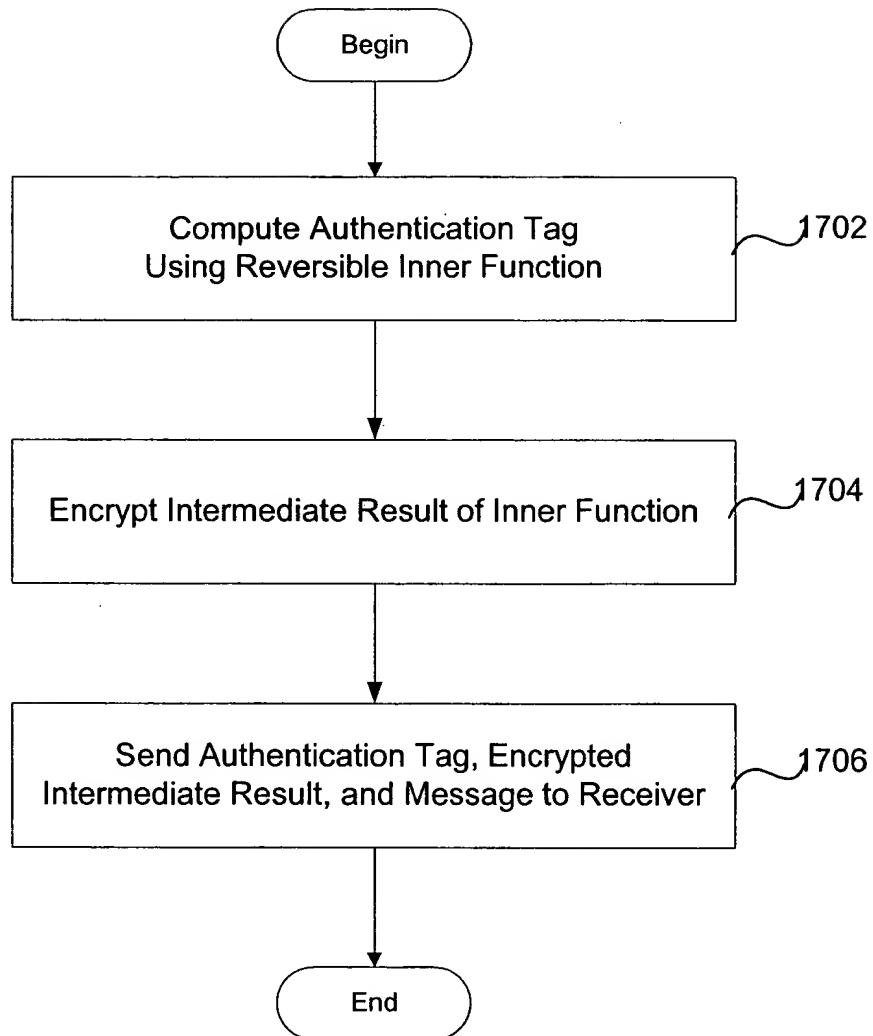


FIG. 17A

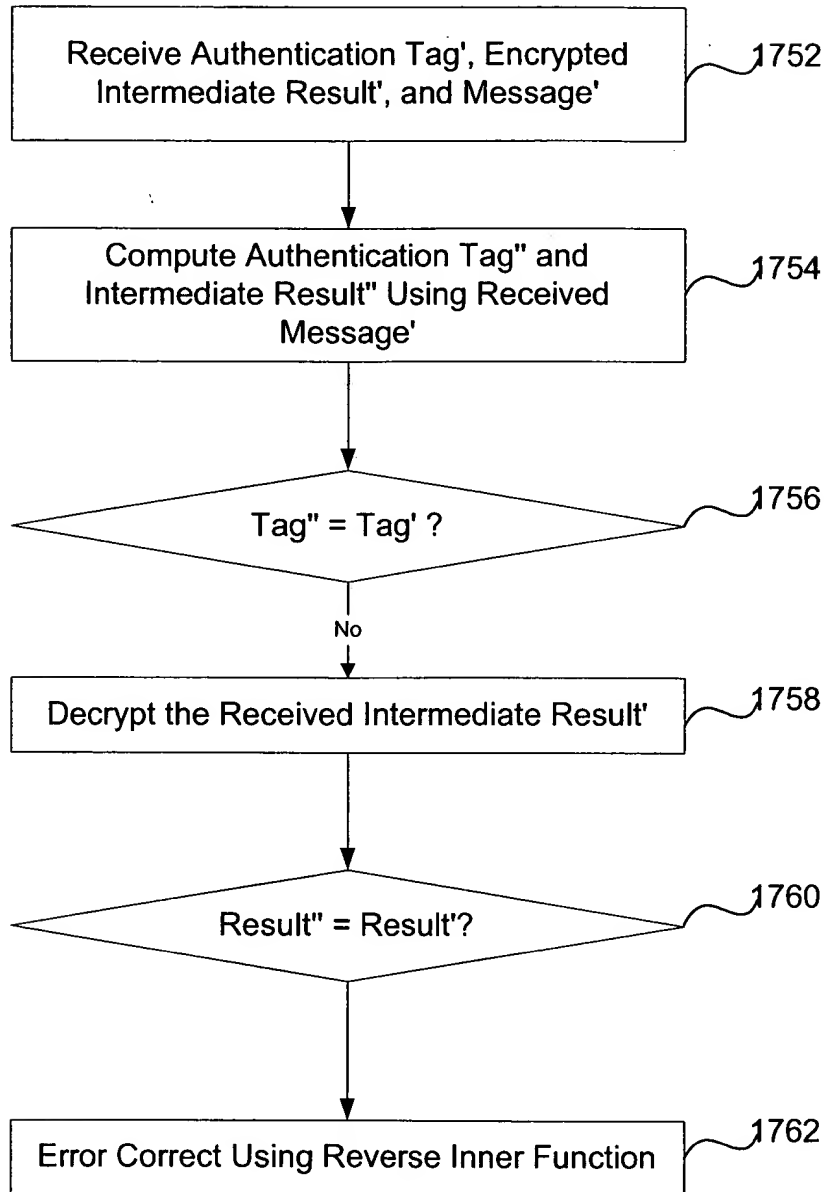


FIG. 17B